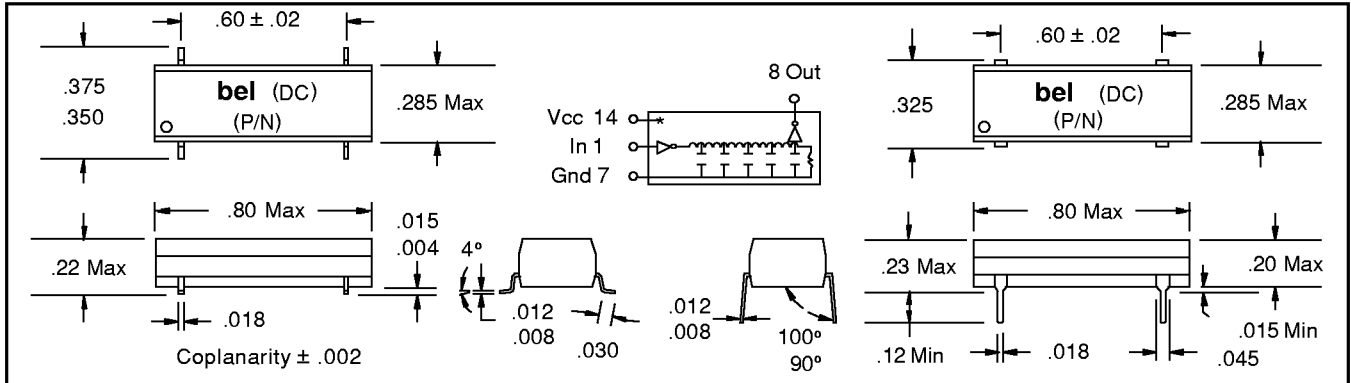


# 1 TAP LEADING EDGE CONTROL LOW VOLTAGE DELAY MODULES



### Part Numbers

SMD	Thru-Hole	Time Delay	Tolerance	Rise Time
S477-0010-06	A477-0010-06	10 ns	± 2.0 ns	3 ns
S477-0020-06	A477-0020-06	20 ns	± 2.0 ns	3 ns
S477-0030-06	A477-0030-06	30 ns	± 2.0 ns	3 ns
S477-0040-06	A477-0040-06	40 ns	± 2.0 ns	3 ns
S477-0050-06	A477-0050-06	50 ns	± 2.5 ns	3 ns
S477-0060-06	A477-0060-06	60 ns	± 3.0 ns	3 ns
S477-0070-06	A477-0070-06	70 ns	± 3.5 ns	3 ns
S477-0080-06	A477-0080-06	80 ns	± 4.0 ns	3 ns
S477-0090-06	A477-0090-06	90 ns	± 4.5 ns	3 ns
S477-0100-06	A477-0100-06	100 ns	± 5.0 ns	3 ns
S477-0125-06	A477-0125-06	125 ns	± 6.3 ns	3 ns
S477-0150-06	A477-0150-06	150 ns	± 7.5 ns	3 ns
S477-0200-06	A477-0200-06	200 ns	± 10.0 ns	3 ns

### Test Conditions @ 25°C

Ein	Pulse Voltage	3.0 Volts
Trin	Rise Time	3.0 ns (10%-90%)
PW	Pulse Width	1.2 x Total Delay
FP	Pulse Period	4 x Pulse Width
Iccl	Supply Current	25 ma Typical
Vcc	Supply Voltage	5.0 Volts

### Electrical Characteristics

	Min.	Max.	Units
Vcc	3.0	3.6	V
Vih	2.4		V
Vil		0.8	V
Iik		-20	ma
Ioh		20	ma
Iol		-20	ma
Voh	2.9		V
Vol		0.1	V
Vik		-0.5	V
Iih		1	ua
Iil		-1	ua
Icch		10	ma
Iccl		30	ma
Ta	0°	70°	C
PW	40		%
d		50	%
Tc	Temp. Coeff. of Total Delay (TD) 100 + (25000/TD) PPM/°C		

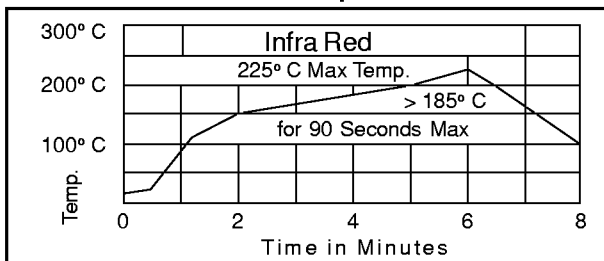
### Tolerances

Input to Output ± 2 ns or 5% , Whichever is Greater  
Delays measured @ 50% levels on Leading Edge only  
with no loads on Output  
Rise and Fall Times measured from 10% to 90% levels

### Drive Capabilities

Nh	Logic 1 Fanout	-	10 LSTTL Loads Max.
Nl	Logic 0 Fanout	-	10 LSTTL Loads Max.

### Recommended Temperature Profile



### Notes

Transfer molded for better reliability  
Compatible with TTL & DTL circuits  
Terminals: Electro-Tin plate phosphor bronze  
Performance warranty is limited to specified parameters listed  
SMD - Tape & Reel available:  
32mm Wide x 16mm Pitch, 500 pieces per 13" reel

## Other Delays and Tolerances Available Consult Sales

Specifications subject to change without notice.

#### Corporate Office

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